Claims

[c1] 1. A driving method for a light-emitting device, suitable for use in an active matrix light-emitting display, comprising:
providing a driver circuit to control the light-emitting device, the driver circuit comprising a data input terminal for inputting a data signal, so as to control the light-emitting status of the light-emitting device;
providing a clock and partitioning the clock into a first clock and a second clock, wherein the first and the second clocks have the same frequencies but are asynchronous to each other;
inputting the data signal to the data input terminal of the driver circuit at the first clock; and
inputting the reset signal to the data input terminal of the driver circuit at the second clock.

[c2] 2. The driving method according to claim 1, wherein the light-emitting device includes an organic light-emitting diode.

[c3] 3. The driving method according to claim 1, wherein the frequencies of the first and second clocks are the same of an image display frequency set up by the active matrix light-emitting display.

[c4] 4. The driving method according to claim 1, wherein the frequencies of the first and second clocks are 60Hz.

[c5] 5. The driving method according to claim 1, wherein the clock is double of the first clock.

[c6] 6. The driving method according to claim 1, wherein the light-emitting device includes an organic light-emitting display to construct a thin-film transistor active matrix organic light-emitting diode display.

[c7] 7. The driving method according to claim 1, wherein the reset signal temporarily switches off a driving transistor used to drive the light-emitting device.

[c8] 8. The driving method according to claim 7, wherein the reset signal is used to

switch on and off the light-emitting device.

[c9] 9. The driving method according to claim 7, wherein the reset signal includes a negative voltage.

[c10] 10. The driving method according to claim 1, wherein the reset signal enables a capacitor of the driver circuit to discharge, wherein the capacitor is used to maintain a voltage for switching a driving device of the driver circuit, so as to switch on the light-emitting device.

[c11] 11. The driving method according to claim 1, wherein the data signal after being decoded and processed includes a plurality of gray scale signals corresponding to a plurality of pixels of the active matrix light-emitting display.

[c12] 12. A driving method for a light-emitting device, applicable to an active matrix light-emitting display system that includes a video control unit receiving a continuous video signal with a frame as the unit, the frame being input with an image display clock, wherein the image display clock outputs an image signal to an active matrix light-emitting display via a clock control unit after performing a decoding and signal process, the driving method comprising:
fixing a reset clock after the clock control unit outputs the image signal and before the frame is changed, a reset signal corresponding to the frame is output to the active matrix light-emitting display to temporarily switch off a plurality of pixel units corresponding to the frame, wherein the pixel units use one frame as the unit to display an image of the frame.

[c13] 13. The driving method according to claim 12, wherein the reset clock and the image display clock are spaced by a half clock of the image display clock.

[c14] 14. The driving method according to claim 12, wherein the reset clock and the image display clock share a common clock by partitioning.

[c15] 15. The driving method according to claim 14, wherein the common clock is double of the image display clock.

[c16] 16. An active matrix light-emitting display system using a frame as a unit to continuously receive a video signal, wherein the frame uses an image display

clock to input, the system comprising:

a color decoding unit, extracting an image signal from the video signal to perform decoding;

a buffer memory unit, temporarily storing an image data obtained by decoding and processing the image signal;

an active matrix light-emitting display;

a clock control unit, extracting the image data from the buffer memory unit, the clock control unit uses the image display clock to output the image signal to the active matrix light-emitting display;

wherein after the clock control unit outputs the image data and before the frame is changed, a reset signal corresponding to the frame is output to the active matrix light-emitting display to switch off a plurality of pixel units of the active matrix light-emitting display corresponding to the frame, wherein the pixel units use a frame as a unit to display an image of the frame.

[c17] 17. The system according to claim 16, wherein the reset clock and the image display clock is spaced from each other by a half of the image display clock.

[c18] 18. The system according to claim 16, wherein reset clock and the image display clock share a common clock by partitioning.

[c19] 19. The system according to claim 16, wherein the common clock is double of the image display clock.

[c20] 20. The system according to claim 16, wherein the clock control unit includes a chip to output the reset signal and the image data at the reset clock and the image display clock.